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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction
Edge and Method of Manufacturing
the Same

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Group Art Unit: 2815

Examiner: Nguyen, Joseph H.

Atty. Docket: MICS:0114
03-0027

Mail Stop Amendment
Commissioner for Patents
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37 C.F.R. 1.8

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Date

Kerri Hyland
Kerri Hyland

Sir:

RESPONSE AND AMENDMENT

In response to the Official Action mailed on February 14, 2005, Applicants respectfully request reconsideration of the above-identified application in view of the remarks and amendments set forth below.